AMENDMENTS TO THE CLAIMS

Please amend claims 1 and 8, to read as follows:

1. (Currently Amended) An electronic apparatus ~~that supplies power to~~ having a semiconductor integrated circuit and power supplying means, the apparatus comprising:

a main circuit, disposed in the semiconductor integrated circuit, the main circuit having at least a CPU, power being supplied to the main circuit through ~~first~~ the power supplying means; and

a standby controlling circuit disposed in the semiconductor integrated circuit, power being supplied to the standby controlling circuit through ~~second~~ the power supplying means ~~that is different from the first power supplying means~~ when the standby control circuit is in ~~for changing~~ an operation mode and a standby mode;

wherein when the standby controlling circuit receives a standby command from the CPU, the standby ~~control~~ controlling circuit turns off the ~~first power supplying means to turn off~~ power ~~of~~ supplied to the main circuit~~,~~ by the power supplying means and goes into the standby mode;

wherein when a wakeup cause takes place, the standby controlling circuit holds information about the wakeup cause in the standby mode ~~in the standby mode~~ and turns on the ~~first~~ power supplied to the main circuit by the power supplying means ~~to turn on power of the main circuit,~~; and

~~where~~ wherein the CPU of the main circuit identifies the wakeup cause and executes a process corresponding to the identified wakeup cause; and

wherein the standby controlling circuit goes into the operation mode.

2. (Canceled)

3. (Canceled)

4. (Canceled)

5. (Canceled)

6. (Canceled)

7. (Canceled)

8. (Currently Amended) A power supplying method for an electronic apparatus having a power supplying means and a semiconductor integrated circuit composed of a main circuit and a standby controlling circuit, wherein the ~~whose~~ power of the standby controlling circuit provided by the power supplying means is always turned on, the main circuit having a CPU, the method comprising the steps of:

when the CPU of the main circuit in the semiconductor integrated circuit issues a standby command, causing the standby controlling circuit in the semiconductor integrated circuit to turn off power ~~of~~ provided to the main circuit by the power supplying means, wherein the power provided to the standby controlling circuit by the power supplying means is maintained ~~to enter the main circuit into a standby mode~~;

when ~~the~~ a wakeup cause takes place, causing the standby controlling circuit to hold information about the wakeup cause and turn on power ~~of~~ provided to the main circuit by the power supplying means; and

causing the CPU of the main circuit to identify the wakeup ~~case~~cause and ~~executed~~ execute a process corresponding to the identified wakeup cause.